

## Faculty of Computer Science

At the **Institute of Computer Engineering** the **Chair of Compiler Construction** in a collaborative project for novel programming methodologies for emerging computer architectures in the context of future 5G communication standards and beyond offers a fixed-term position as

### **Research Associate** (m/f/x)

(subject to personal qualification employees are remunerated according to salary group E 13 TV-L)

starting **April 15, 2022**.

*Research areas:* **Programming languages, source-to-source compilation, code optimization, emerging computing systems.**

*Terms:* The position is limited until October 15, 2024 with the option for extension. The period of employment is governed by the Fixed Term Research Contracts Act (Wissenschaftszeitvertragsgesetz – WissZeitVG).

## Position and Requirements

At the Chair of Compiler Construction, we have the long-term vision of shaping how future electronic systems are programmed. This includes defining novel programming abstractions through domain-specific languages and associated compiler infrastructures to enable optimizing software for heterogeneous computing systems in the embedded and high-performance computing domains. In this context, we are looking for a highly motivated researcher to work on a collaborative project, with partners from industry and academia, that seeks to re-define the computer and system architecture for future wireless communication systems. The candidate will interact with experts in communication standards and with researchers defining extreme energy-efficient edge servers in virtualized radio access networks (vRANs), for applications in the areas of autonomous mobility, health monitoring of ageing societies and Industry 4.0. We are looking for a candidate with expertise in system-level optimization methodologies, compilation, and with a strong interdisciplinary attitude towards research. Knowledge of streaming and dataflow programming models is extremely beneficial. The selected candidate will work with state-of-the-art compilation frameworks and with system simulators for heterogeneous systems.

We aim at attracting the best talent in the respective research fields and expect the following:

### **Basic Requirements:**

- an outstanding university degree (master's/diploma or equivalent) in computer science, mathematics, electrical engineering or a relevant area;
- an independent, target- and solution-driven work attitude;
- an integrative and cooperative personality with excellent communication and social skills;
- fluency in English - written and oral.

### **Preferred Qualification:**

- knowledge of programming languages and methods;
- strong background in computer architecture;
- knowledge of LLVM, Clang and/or MLIR;
- knowledge of fundamentals of signal processing systems;
- Initial record of creative research.

Informal enquiries can be submitted to Prof. Dr.-Ing. Jerónimo Castrillón, Tel +49 (351) 463 42716; Email: [jeronimo.castrillon@tu-dresden.de](mailto:jeronimo.castrillon@tu-dresden.de)

Applications from women are particularly welcome. The same applies to people with disabilities.

## What we offer

You will join a team of enthusiastic researchers who creatively pursue their individual research agendas. The chair is a part of the "Center for Advancing Electronics Dresden", which offers plenty of resources and structures for career development.

## Application Procedure

Complete applications (in English only) including motivation letter, CV, copy of degree certificate, transcript of grades (i.e. the official list of coursework including your grades) and proof of English language skills should be submitted preferably via the TU Dresden SecureMail Portal <https://securemail.tu-dresden.de> by sending it as a single pdf document quoting the reference number **PhD2201-CCC** in the subject header to [recruiting.cfaed@tu-dresden.de](mailto:recruiting.cfaed@tu-dresden.de) or alternatively by post to: **TU Dresden, cfaed, Professur für Compilerbau, Herrn Prof. Jeronimo Castrillon, Helmholtzstr. 10, 01069 Dresden, Germany**. The closing date for applications is **March 01, 2022** (stamped arrival date of the university central mail service applies). Please submit copies only, as your application will not be returned to you. Expenses incurred in attending interviews cannot be reimbursed.

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**Reference to data protection:** Your data protection rights, the purpose for which your data will be processed, as well as further information about data protection is available to you on the website: <https://tu-dresden.de/karriere/datenschutzhinweis>

## About cfaed

The cfaed is a cluster which brings together 200 researchers from TU Dresden and ten other research institutions in the areas of Electrical and Computer Engineering, Computer Science, Materials Science, Physics, Chemistry, Biology, and Mathematics. The cfaed addresses the advancement of electronic information processing systems through exploring new technologies which overcome the limits of today's predominant CMOS technology. [www.tu-dresden.de/cfaed](http://www.tu-dresden.de/cfaed)



## About TU Dresden

The TU Dresden is among the top universities in Germany and Europe and one of the eleven German universities that were identified as an 'elite university' since 2012. As a modern full-status university with 17 faculties it offers a wide academic range making it one of a very few in Germany.